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| **Interrupt No** | **Device** | **IN/OUT** | **Description** | **Connection**  **SP0–5 or EX1–6** |
| 0x00 | MMU-Memory Mgmt Unit | OUT | Set page in bank 0-7 | EX4 |
| 0x08 | Flip-Flops | OUT | Set or Reset Flip-Flops used by several devices. Each device uses a set of bits. EEprom bit 0, PS/2 KBD v.3 bit 1 | SP5 |
| 0x10 | TFT LCD w SSD1963 | BOTH | Control The TFT Screen | SP4 |
| 0x18 | Input of Devices | IN | Read bits from devices. Used on Interrupt Device Bits 0-2. Also for old PS/2 Keyboard v.3 board. Bits 6 & 7 | SP3 |
| 0x20-0x27 | RS 232 Serial | BOTH | Serial Comms | SP2 |
| 0x28 |  |  |  |  |
| 0x30-0x37 | Storage Card via RS232 | BOTH | Serial Comms to Arduino | SP1 |
| 0x38 |  |  |  | EX3 |
| 0x40 | Interrupt Module | OUT | Clear an Interrupt | EX2 |
| 0x48-0x4B | I2C Module | BOTH | Control I2C Module & devices | EX1 |
| 0x50 |  |  |  |  |
| 0x58 |  |  |  |  |
| 0x60 |  |  |  |  |
| 0x68 |  |  |  |  |
| 0x70 |  |  |  |  |
| 0x78 | PS/2 Keyboard | BOTH | Control the new Ps/2 Keyboard v5 Board | Straight connection |

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| **Interrupt No** | **Device** | **IN/OUT** | **Description** | **Connection**  **SP0–5 or EX1–6** |
| 0x00 | MMU-Memory Mgmt Unit | OUT | Set page in bank 0-7 | SP5 |
| 0x08 | Flip-Flops | OUT | Set or Reset Flip-Flops used by several devices. Each device uses a set of bits. EEprom bit 0 | SP4 |
| 0x10 | TFT LCD w SSD1963 | BOTH | Control The TFT Screen | SP3 |
| 0x18 (+7) | RS 232 Serial | BOTH | Serial Comms | SP2 |
| 0x20 | PS/2 Keyboard | BOTH | Control the new Ps/2 Keyboard v5 Board | SP1 |
| 0x28 (+7) | Storage Card via RS232 | BOTH | Serial Comms to Arduino | SP0 |
| 0x30 |  |  |  |  |
| 0x38 |  |  |  |  |
| 0x40 |  |  |  |  |
| 0x48 |  |  |  |  |
| 0x50 |  |  |  |  |
| 0x58 |  |  |  |  |
| 0x60 |  |  |  |  |
| 0x68 | Interrupt Module | BOTH | Clear an Interrupt, Read Int no Device Bits 0-2. | EX3 |
| 0x70 (+4) | I2C Module | BOTH | Control I2C Module & devices | EX2 |
| 0x78 | Input of Devices | IN | Read bits from devices NOT USED | EX1 |